

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	Method and apparatus for manufacturing circuit board
First Named Inventor/Applicant Name:	Toshiaki Takenaka
Customer Number:	23122
Filer:	Lawrence E. Ashery
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Foreign Reference	MAT-8716US_JP2003033967. PDF	573018 <small>3f8b27623d79223674686a28ec23a2a5a545e8a5ac</small>	no	8

Warnings:

Information:

2	NPL Documents	MAT-8716US_NPL.PDF	31689 ab7768ba9903a95548d798f5919798ba95f1c5 7df	no	1
Warnings:					
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3	Information Disclosure Statement Letter	MAT-8716US_IDS.PDF	64555 f5b641f1825ab6a29652070b1541ab3727155 c18d0	no	2
Warnings:					
Information:					
4	Information Disclosure Statement (IDS) Filed (SB/08)	MAT-8716US_IDS-1449.PDF	68917 d841c9d48e179579513188b1a2d79d2c5 0d8d1	no	1
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